

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended): A method for identifying a failed device in a computer, said method comprising steps of:

(a) providing a basic input-output system (BIOS) memory having a plurality of memory locations containing data values representing a series of computer program instructions for testing a device in said computer and for determining if an error resides in said BIOS memory, wherein a predetermined one of said memory locations contains an error detection value based on the data values in the remaining memory locations of said BIOS memory;

(b) the instructions in said BIOS memory attempting to obtain a model name and specification information of detecting a first device in said computer to determine whether said first device has failed;

(c) providing a single luminescent display which is mounted on said computer; and

(d) if said first device is determined to have failed in step (b), blinking said single luminescent display ON and OFF at a first frequency.

2. (currently amended): The method of claim 1 wherein ~~the step of detecting said first device in said computer further comprises identifying a type and an identification of said first device in said computer using~~ the instructions in said BIOS memory further attempt to obtain manufacturer information of said first device.

3. (currently amended): The method of claim 1 wherein ~~the step (b) of detecting said first device in said computer further comprises analyzing the data values and said error detection value at said predetermined memory location in said BIOS memory to determine whether an error instruction is contained within said BIOS memory.~~

4. (currently amended): The method of claim 1 further comprising the steps of:

(e) providing a sound playing device; and

(f) if said first device is determined to have failed in step (b), driving said sound playing device to beep at said first frequency.

5. (currently amended): The method of claim 1 further comprising the steps of:

(e) said BIOS memory attempting to obtain a model name and specification information of detecting a second device in said computer to determine whether said

second device has failed;

(f) if said second device is determined to have failed in step (e), blinking said single luminescent display ON and OFF at a second frequency different from said first frequency.

6. (previously presented): The method of claim 5 further comprising the ~~step~~ steps of:

(g) providing a sound playing device; and

(h) if said second device is determined to have failed in step (e), driving said sound playing device to beep at said second frequency.

7. (currently amended): The method of claim 5 wherein ~~the step of detecting said second device in said computer further comprises identifying a type and an identification of said second device in said computer using~~ the instructions in said BIOS memory further attempt to obtain manufacturer information of said second device.

8. (currently amended): The method of claim 5 wherein ~~the step (e) of detecting said second device in said computer~~ further comprises analyzing the data values and said error detection value at said predetermined memory location in said

BIOS memory ~~to determine whether an error instruction is contained within said BIOS memory.~~

9. (currently amended): The method of claim 5 further comprising the steps of:

(g) said BIOS memory attempting to obtain a model name and specification information of detecting a third device in said computer to determine whether said third device has failed; and

(h) if said third device is determined to have failed in step (g), blinking said single luminescent display ON and OFF at a third frequency different from said first frequency and said second frequency.

10. (currently amended): The method of claim 9 further comprising the steps of:

(i) providing a sound playing device; and

(j) if said third device is determined to have failed in step (g) ~~detected to contain an error~~, driving said sound playing device to beep at said third frequency.

11. (currently amended): The method of claim 9 wherein ~~the step of detecting said third device in said computer further comprises identifying a type and an identification of said third device in said computer by means of~~ the instructions in

said BIOS memory further attempt to obtain manufacturer information of said third device.

12. (currently amended): The method of claim 9 wherein the step of detecting said third device in said computer further comprises analyzing the data values and said error detection value at said predetermined memory location in said BIOS memory ~~to determine whether an error instruction is contained in said BIOS memory.~~

13. (original): The method of claim 1 wherein said error detection value at said predetermined memory location in said BIOS memory contains a checksum value.

14. (currently amended): A device for displaying a message indicative of a failed device in a computer, comprising:

a single luminescent display which is mounted on said computer and is operable to blink at multiple frequencies;

a basic input-output system (BIOS) memory having a plurality of memory locations containing data values representing a series of computer program instructions for testing a hardware device in said computer, and sending a control signal according to the result of testing said hardware device, wherein the instructions in said BIOS memory determine whether said hardware device has

failed by attempting to obtain a model name and specification information of said hardware device; and

a decoding element which receives said control signal and outputs a driving signal to enable said single luminescent display to blink at a frequency associated with said control signal received thereof if the instructions in said BIOS memory determine that said hardware device has failed.

15. (previously presented): The device of claim 14 wherein said single luminescent display comprises a light-emitting diode mounted on said computer provided for indicating the power status of said computer.

16. (original): The device of claim 14 further comprising a serial interface provided for communication between said BIOS memory and said hardware device.

17. (original): The device of claim 16 wherein said serial interface comprises a system management bus interface.

18. (original): The device of claim 14 wherein said decoding element includes an input/output port for transmitting said driving signal to said single luminescent display.

19. (original): The device of claim 14 wherein said decoding element comprises a bridge chip.

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Application No.: 09/822,875

20. (original): The device of claim 14 further comprising a sound playing device which is drivable to beep at a frequency associated with said control signal received thereof.